1 Activity 1 - Control Bits and Pipeline Registers

The following control bits are set by the Control component, which is part of the ID phase:

- RegDst
- Branch
- Jump
- MemRead
- MemWrite
- MemToReg
- ALUop
- bNegate
- ALUsrc
- RegWrite

Figure out what phase uses each control bit. Then, list all of the control bits which must be stored in each pipeline register.

Also list any fields that you need to store from the instruction. For instance, when do we actually use the rd field in an R-format instruction?